

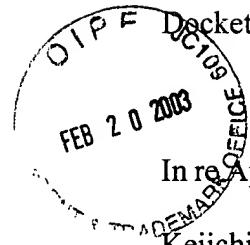
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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re Application of

Keiichiro WAKAMIYA, et al.

Serial No.: 09/818,906

Filed: March 28, 2001

For: SEMICONDUCTOR DEVICE

: RESPONSE UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE

: Group Art Unit: 2811

: Examiner: Nitin Parekh

Entered  
J. manillan  
5/31/03

Box AF  
Commissioner for Patents  
Washington, DC 20231

Sir:

The following Amendment and Remarks is submitted in response to the Office Action dated November 20, 2002 pursuant to the provisions of 37 CFR § 1.116.

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IN THE CLAIMS:

Please amend claims 1 and 7 as follows:

1. (Twice Amended) A semiconductor device comprising:

a semiconductor chip,

a protective insulating layer comprising a coating layer covering the surface of the semiconductor chip;

a plurality of connecting conductors connected to the surface of the semiconductor chip and penetrating the [protective insulating] coating layer beyond the outside surface of the [protective insulating] coating layer;